

FIG. 1

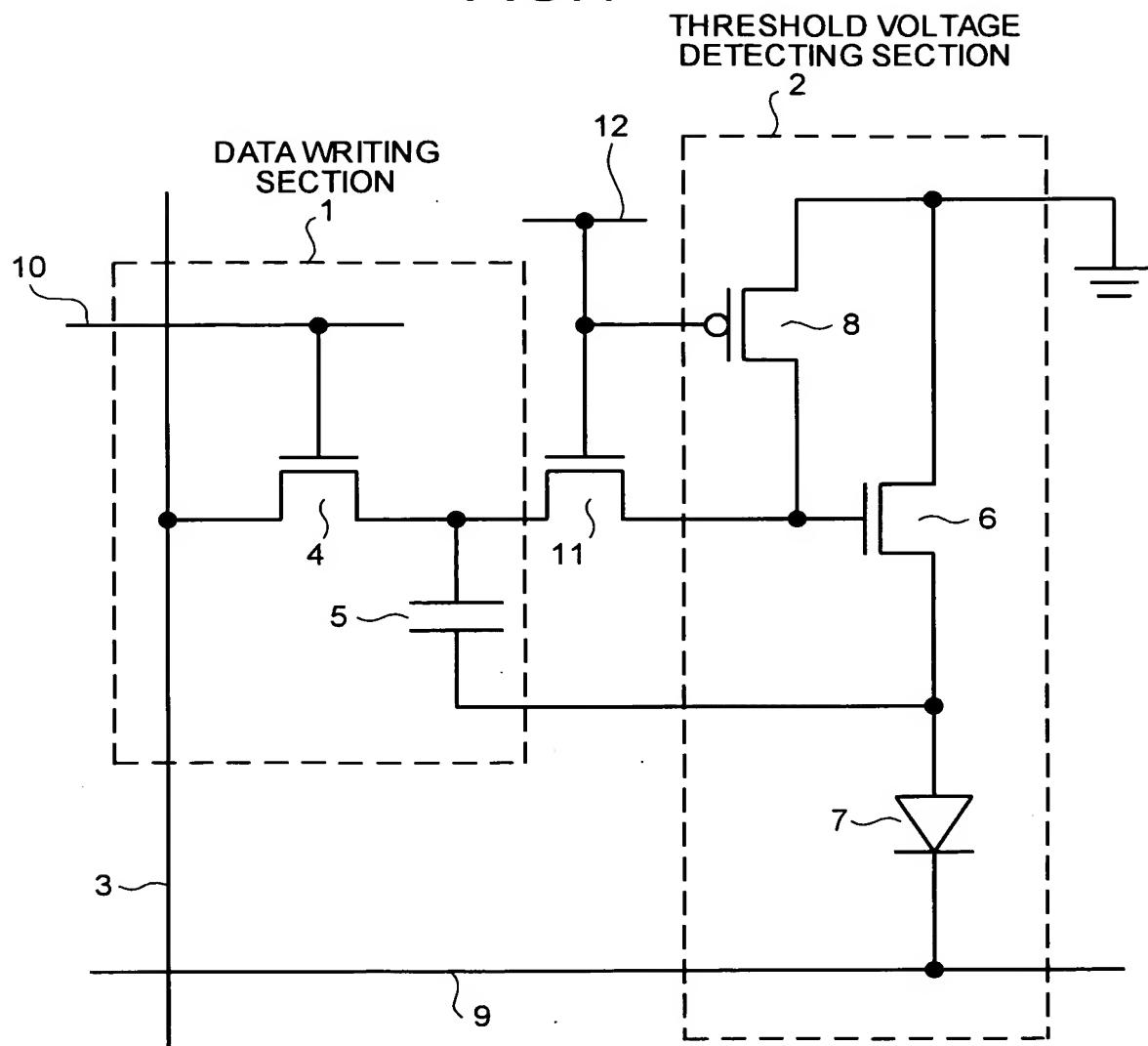
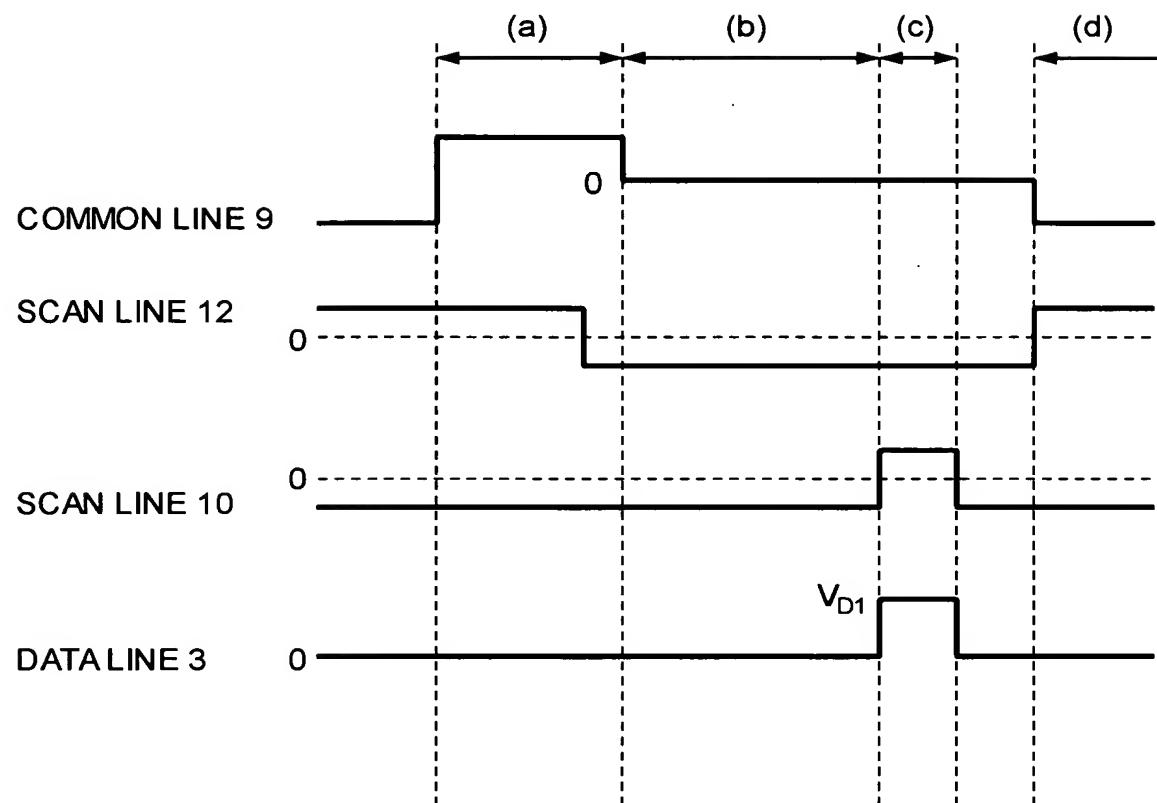


FIG.2



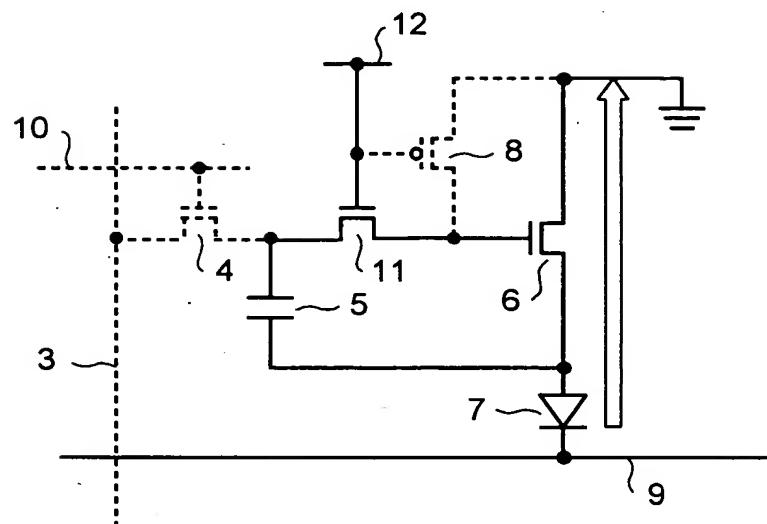
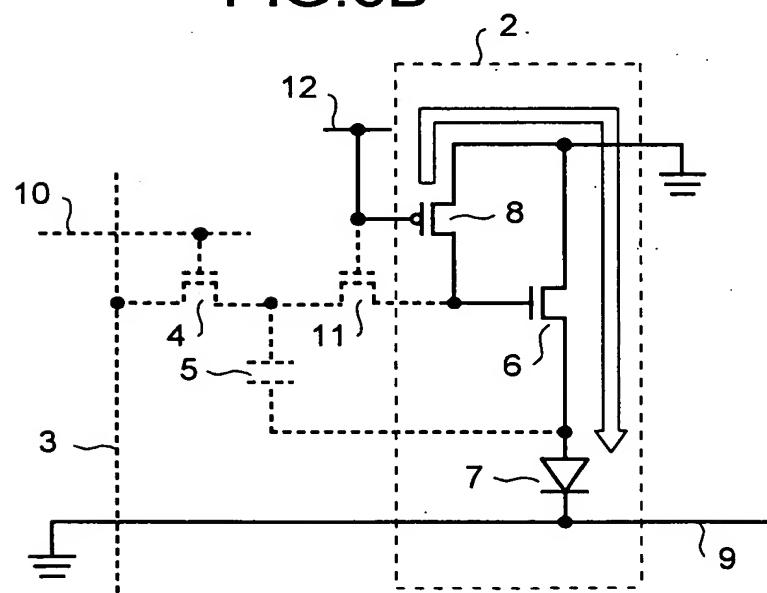
**FIG.3A****FIG.3B**

FIG.3C

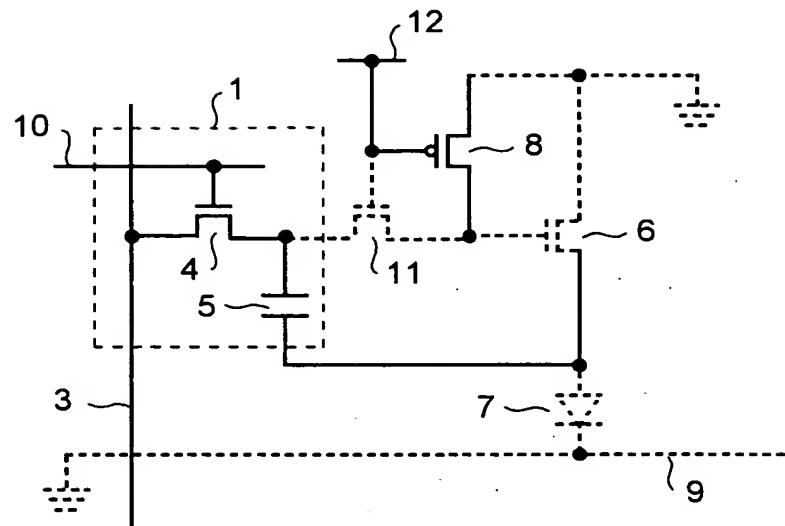


FIG.3D

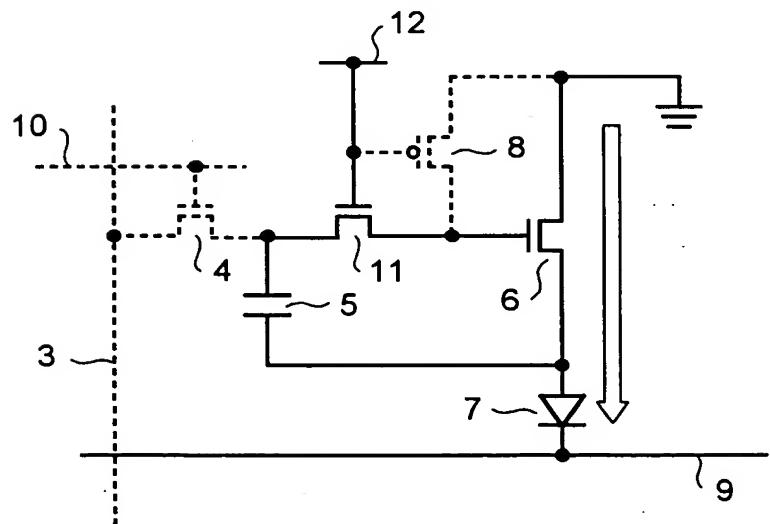


FIG.4

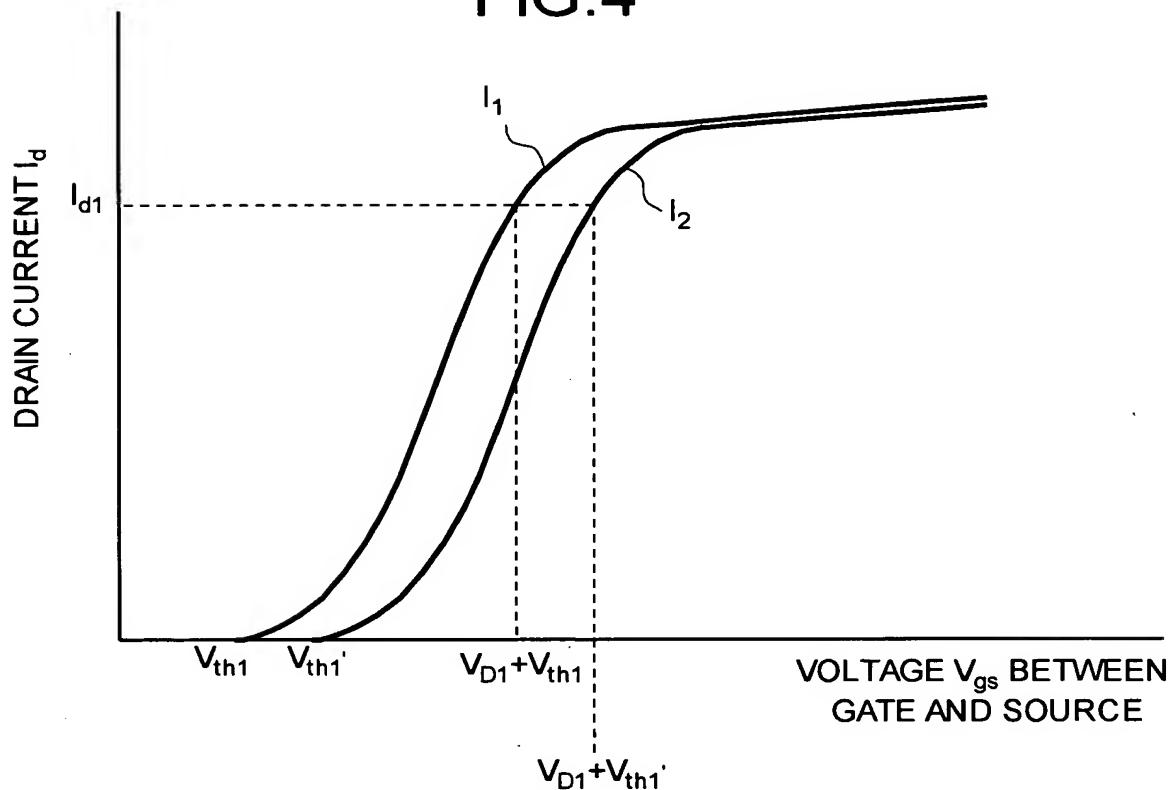


FIG.5

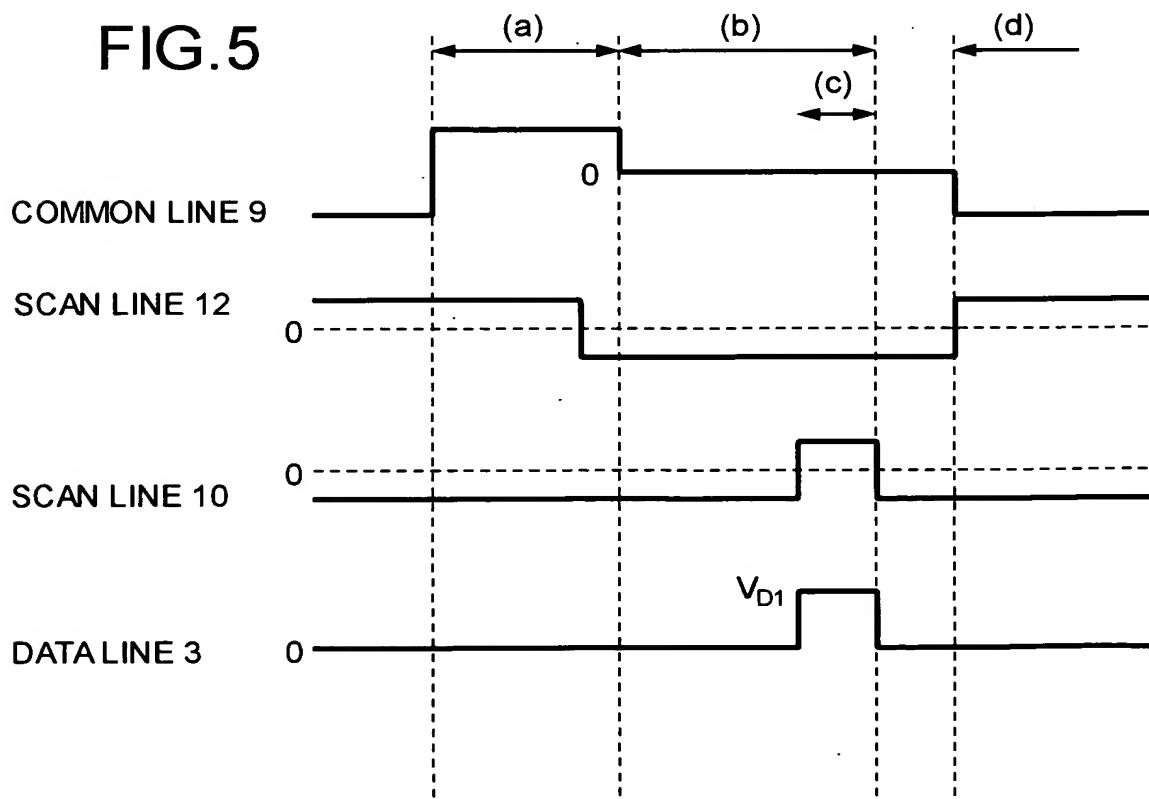


FIG.6

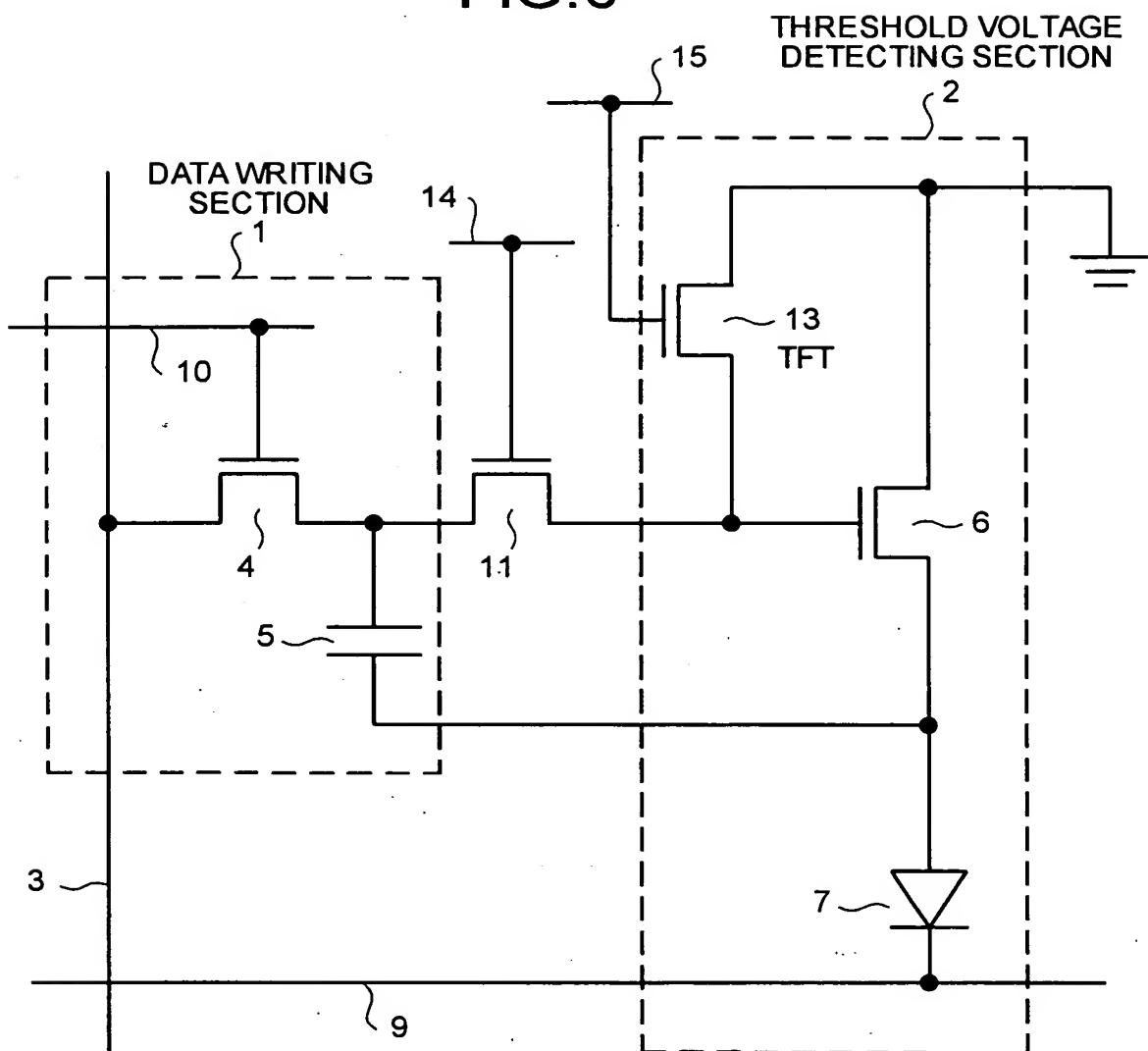


FIG.7

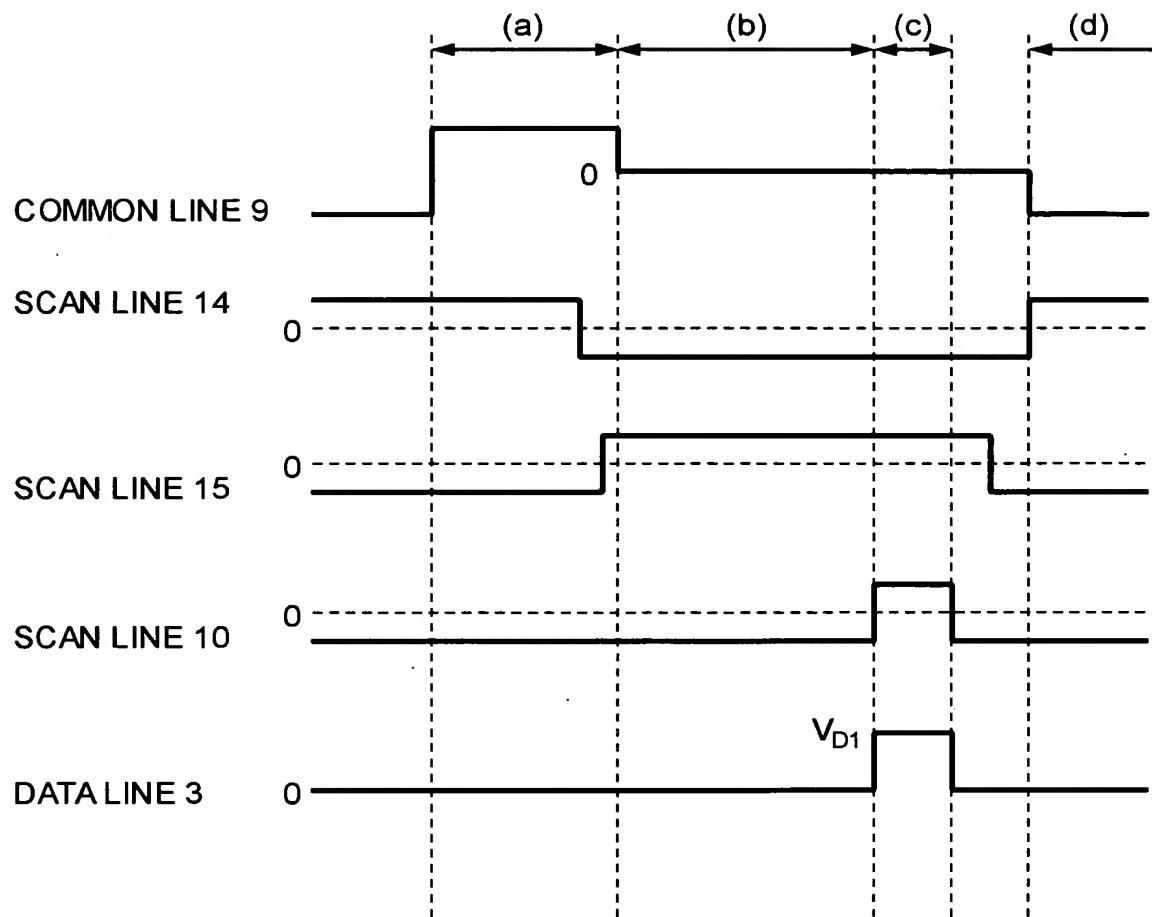


FIG.8

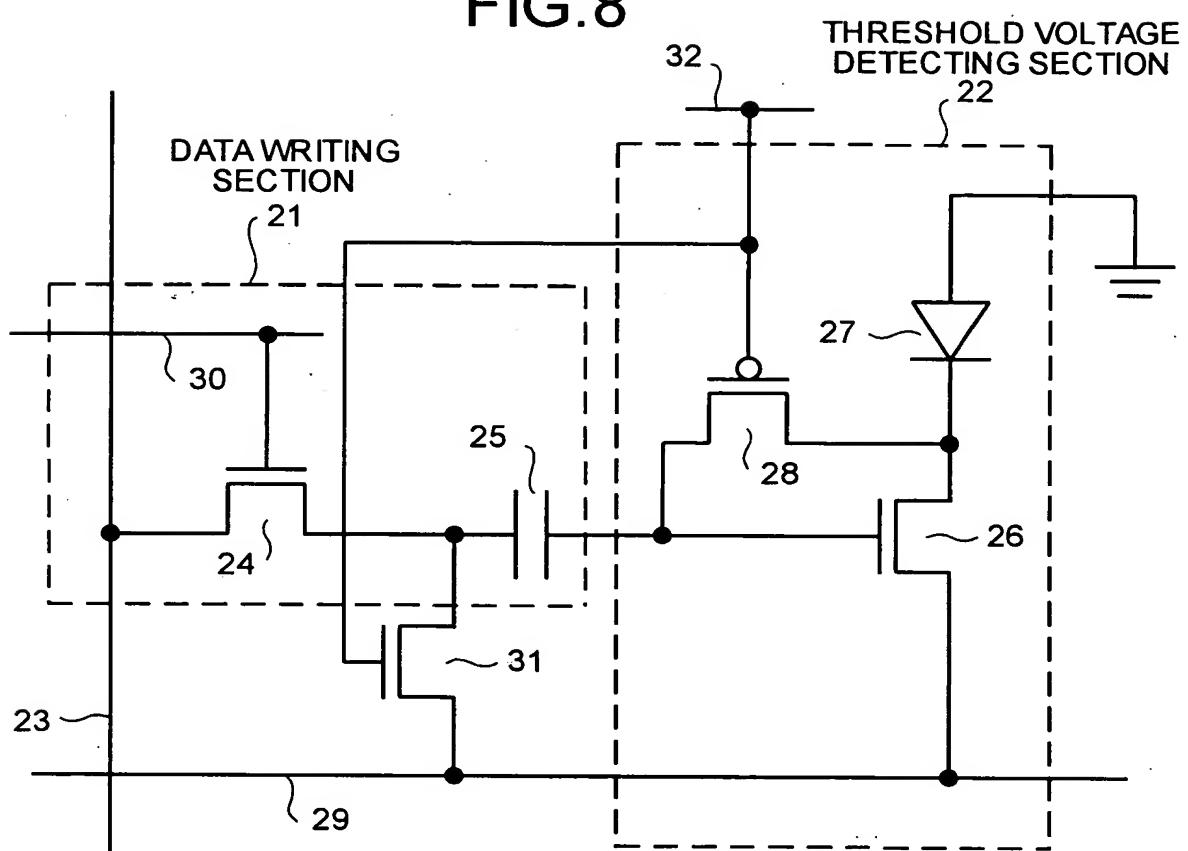
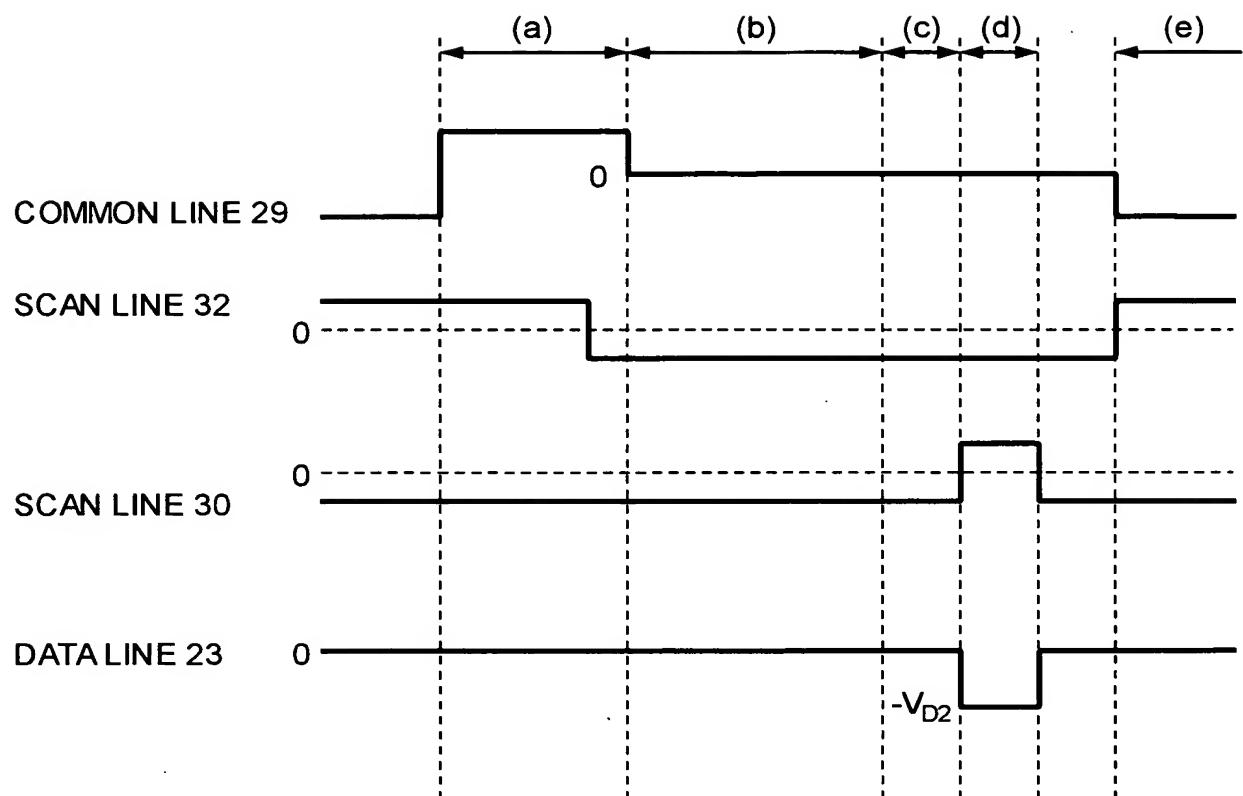


FIG. 9



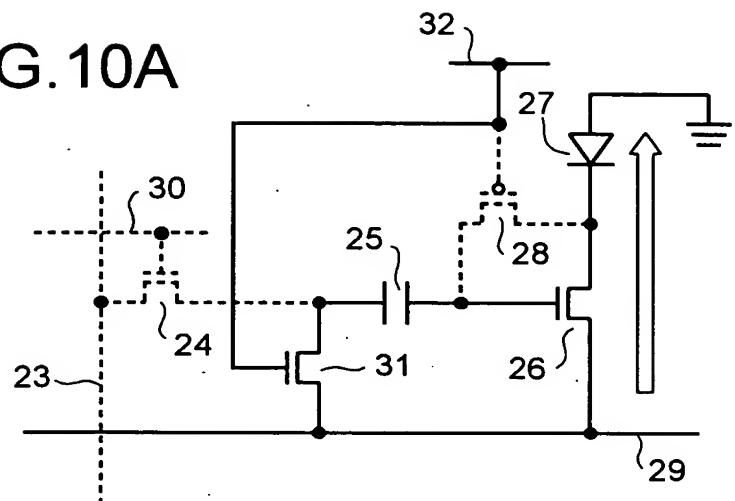
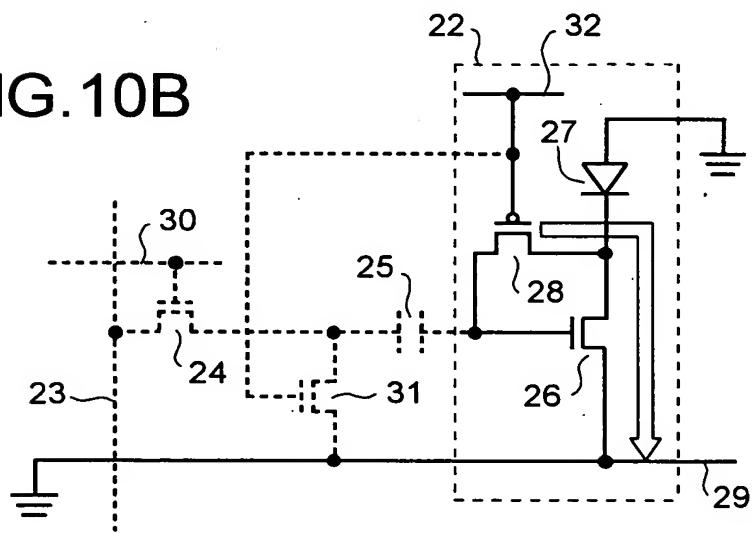
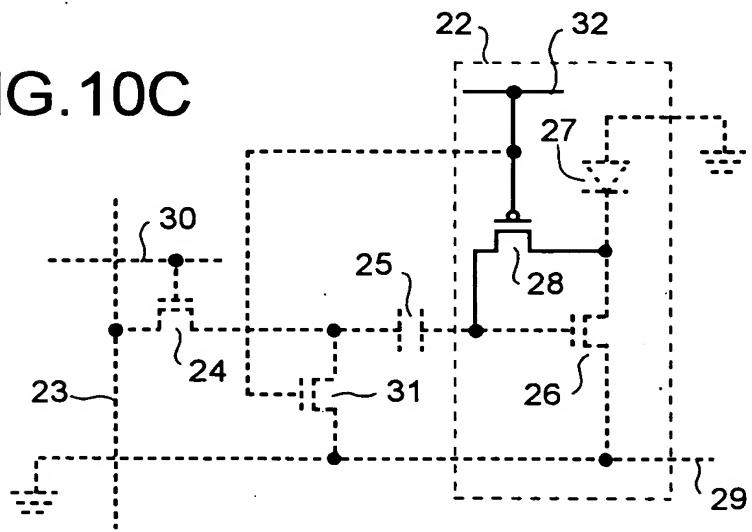
**FIG.10A****FIG.10B****FIG.10C**

FIG.10D

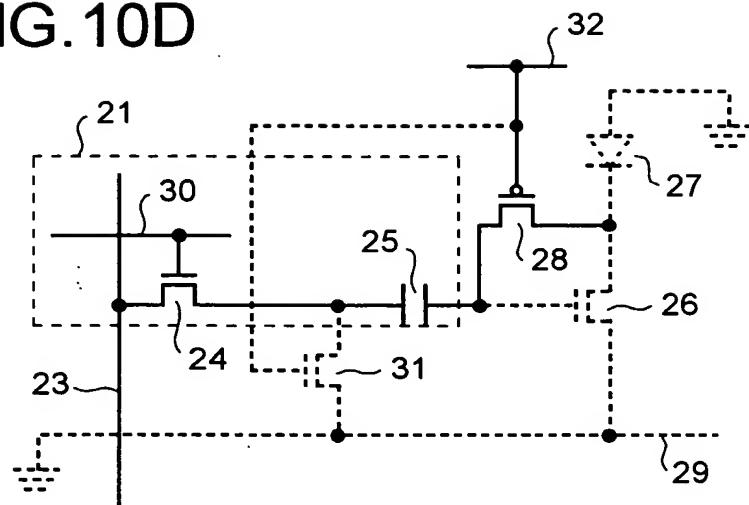


FIG.10E

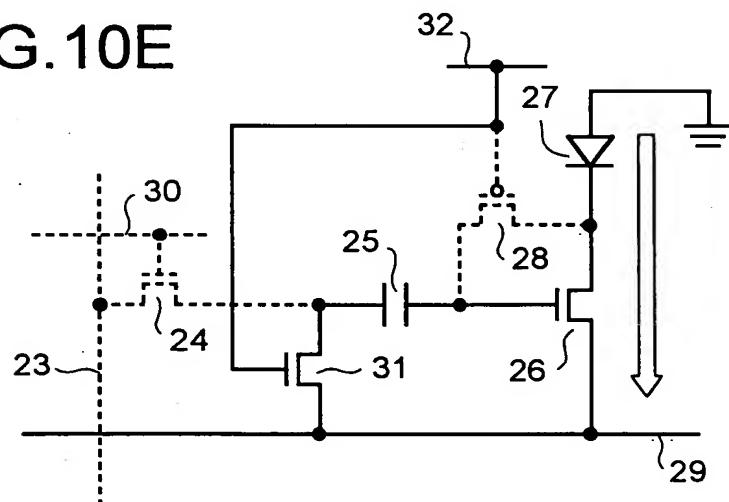


FIG. 11

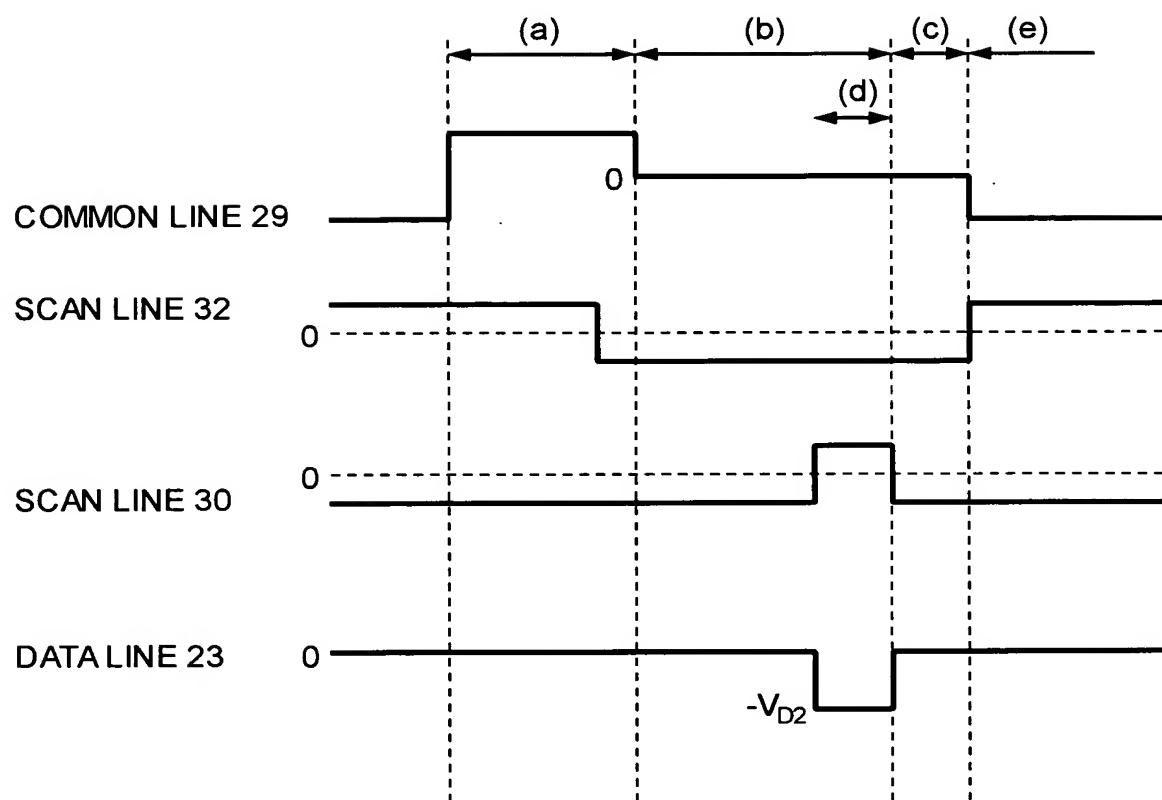


FIG. 12

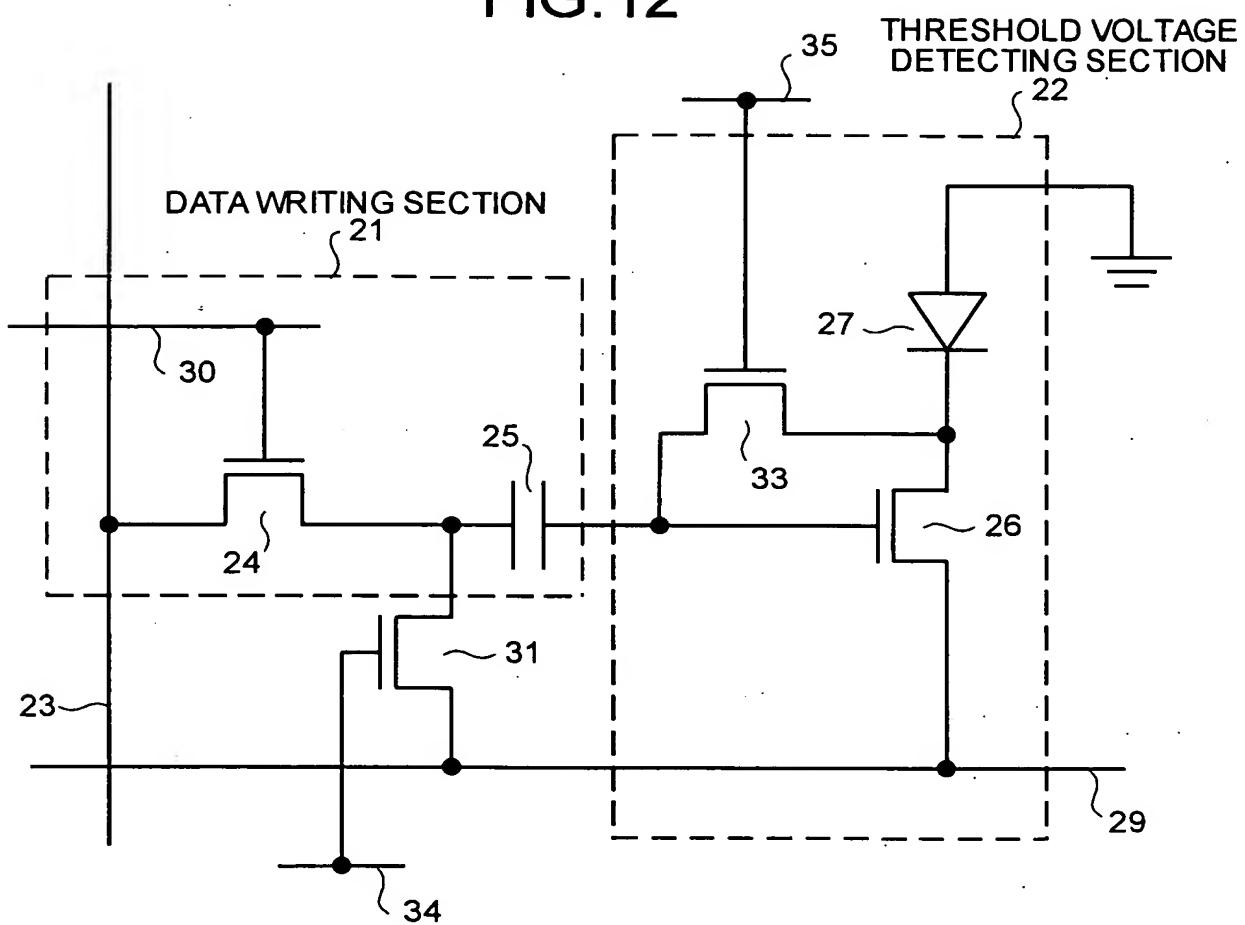


FIG.13

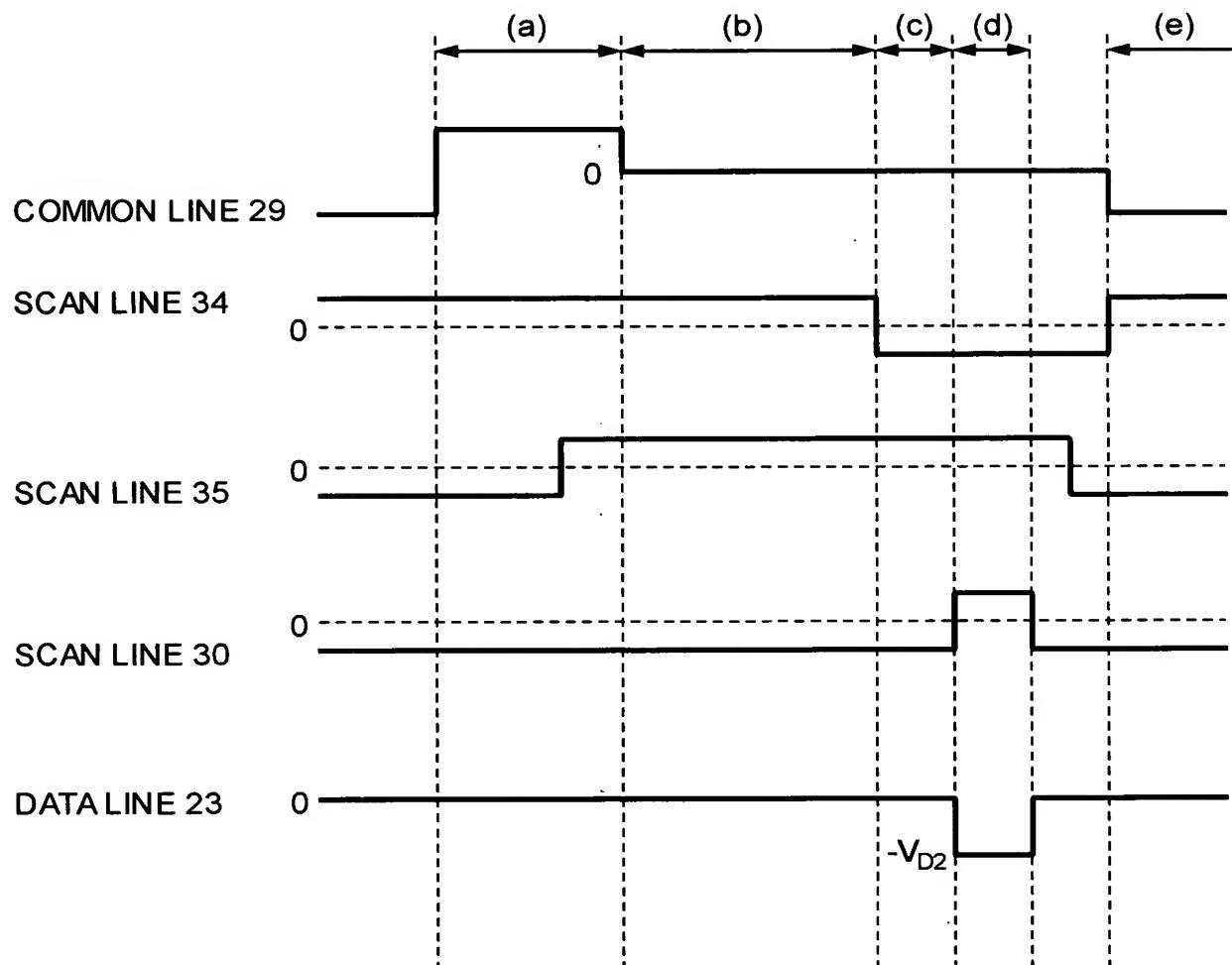


FIG.14

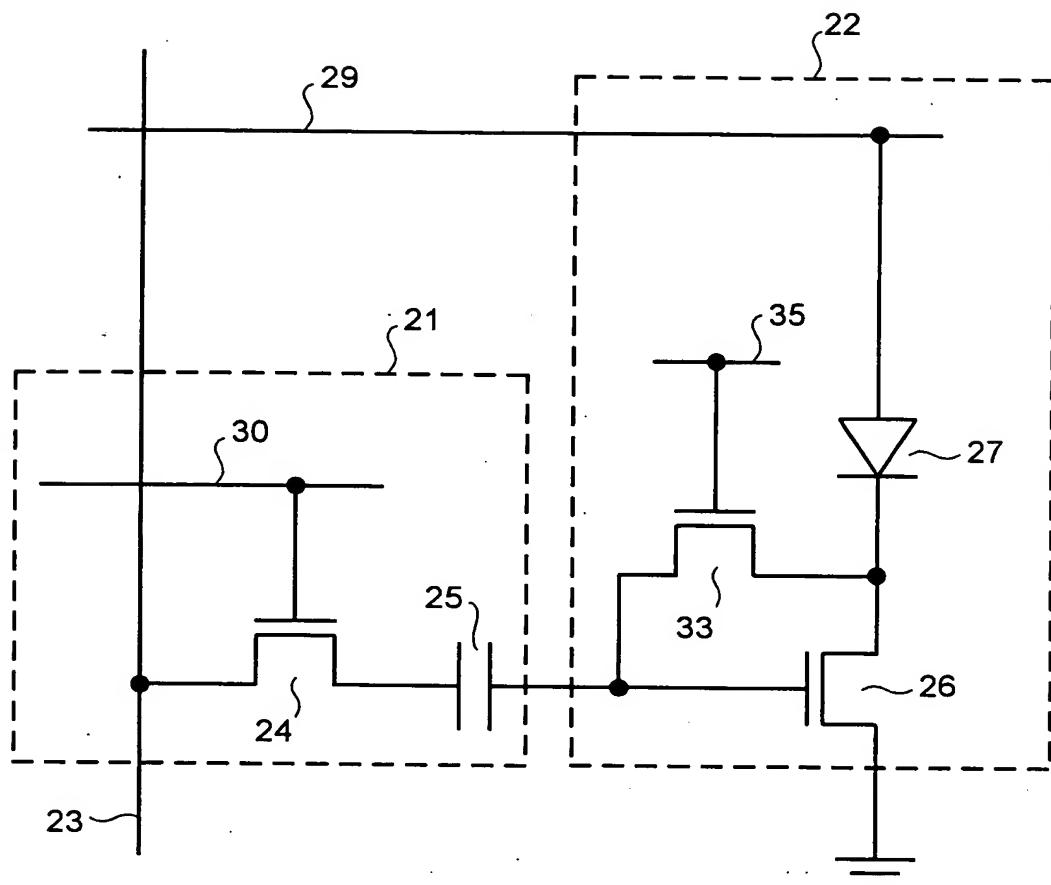


FIG. 15

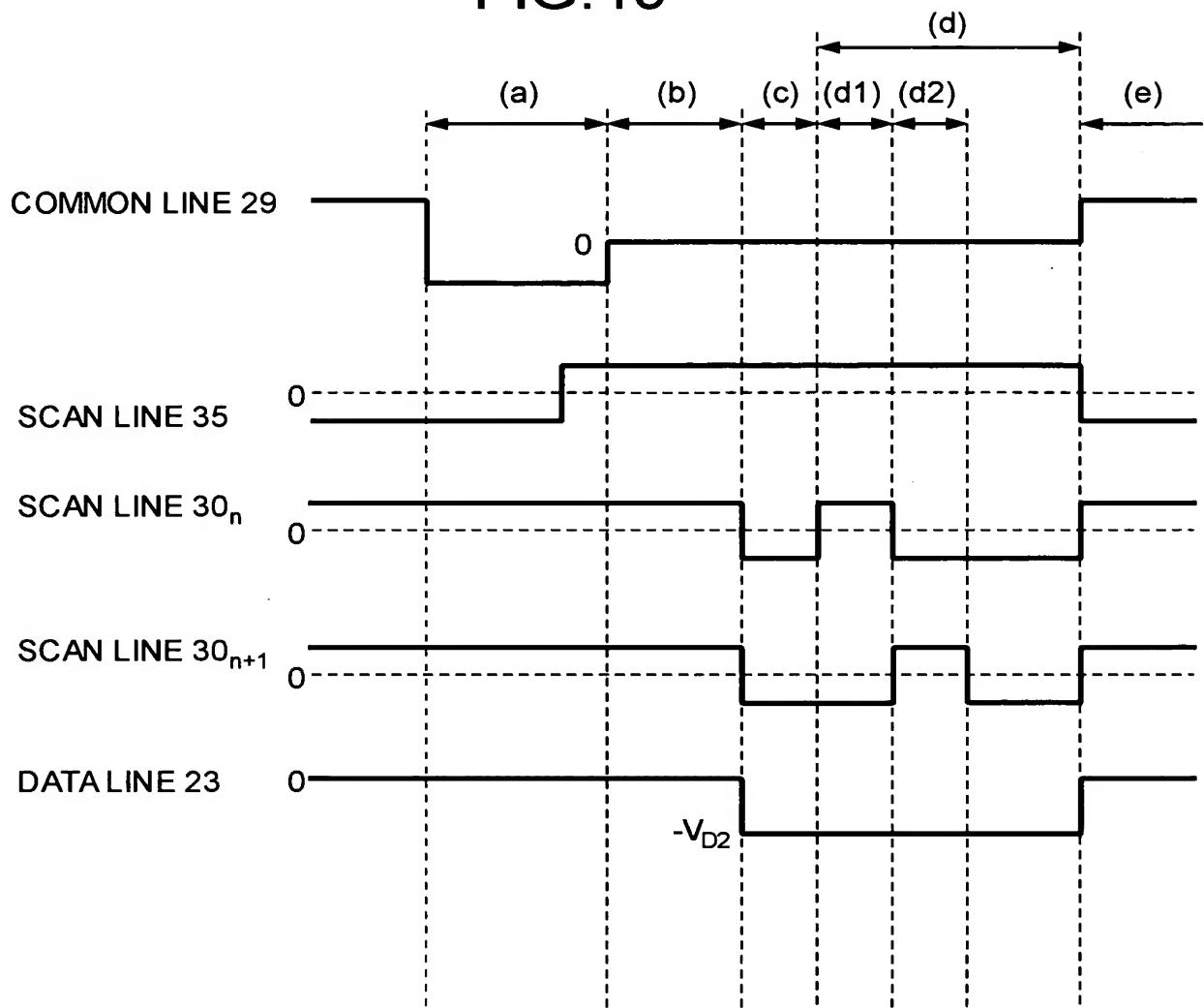


FIG.16A

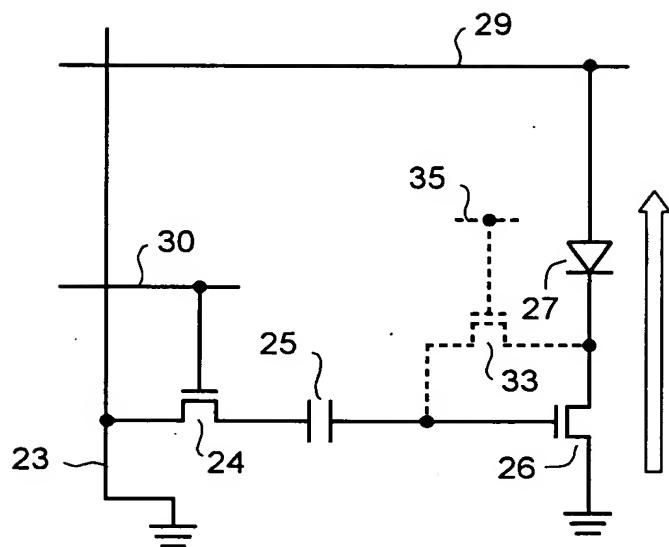


FIG.16B

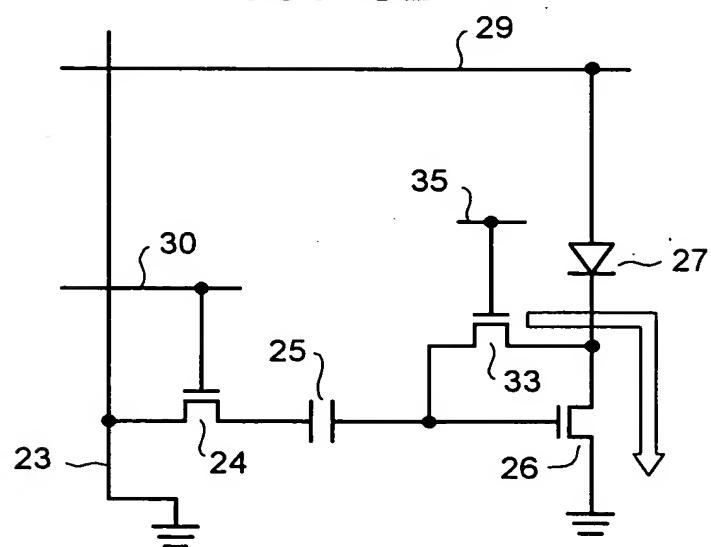


FIG.16C

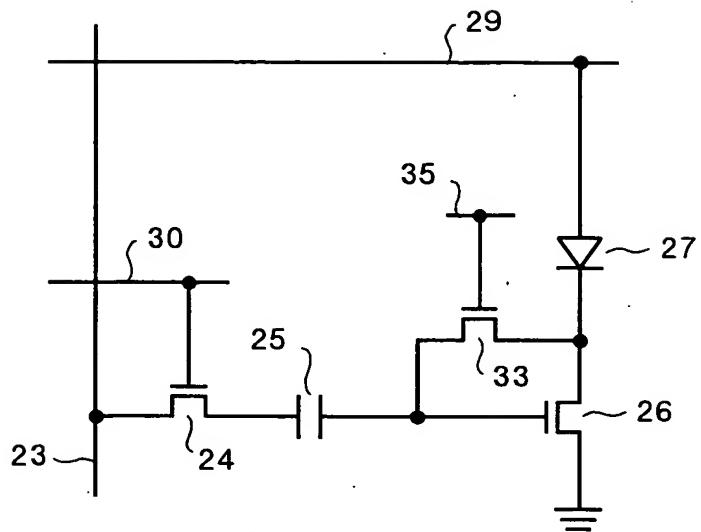


FIG.16D

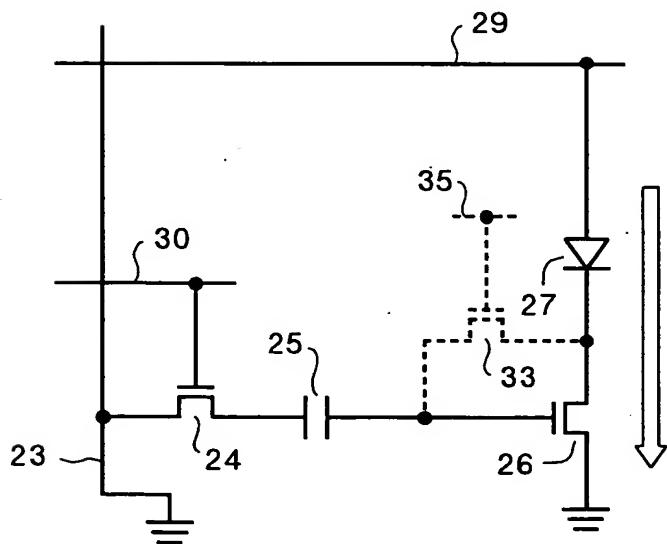


FIG.17

## THRESHOLD VOLTAGE DETECTING SECTION

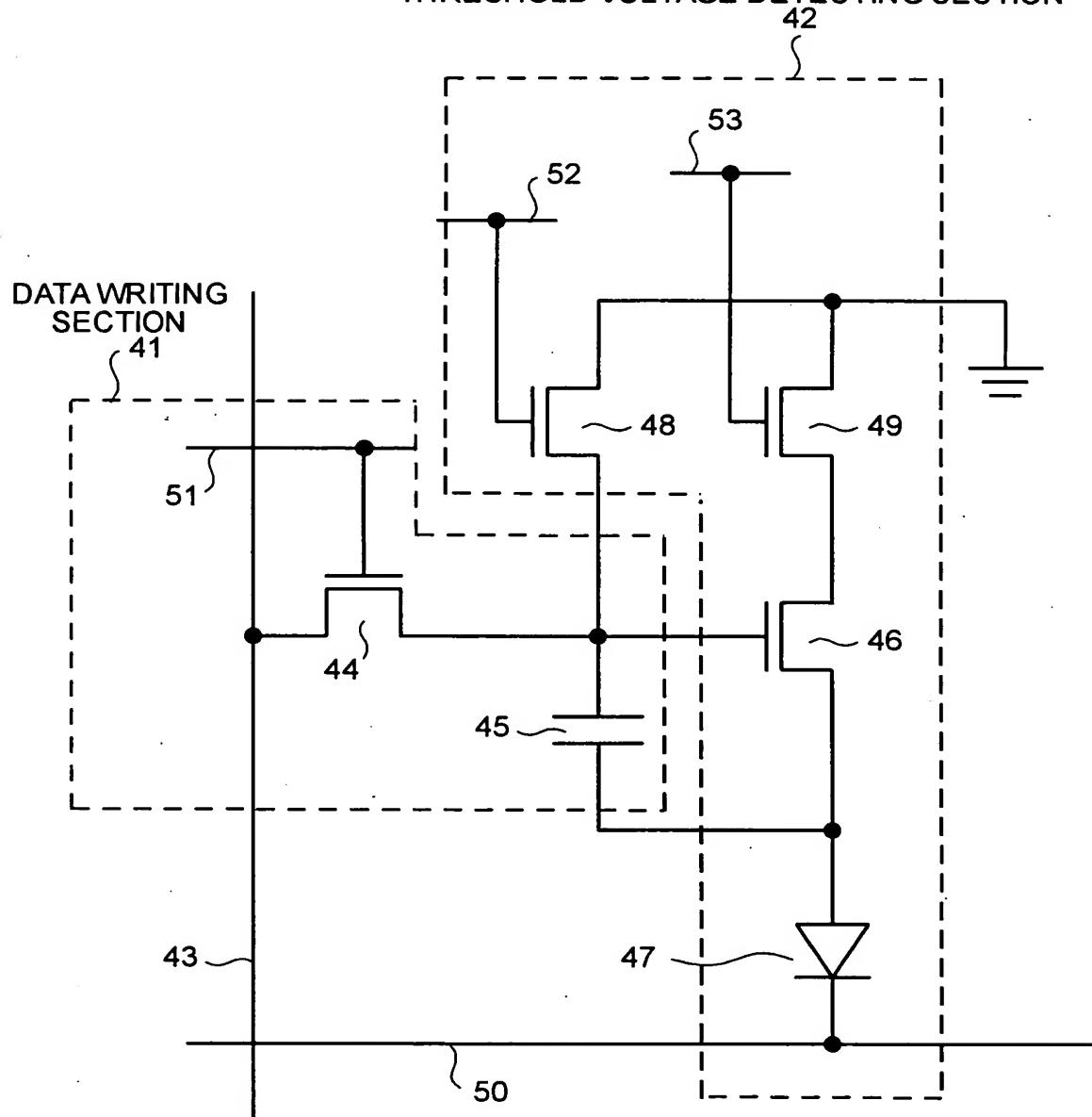


FIG.18

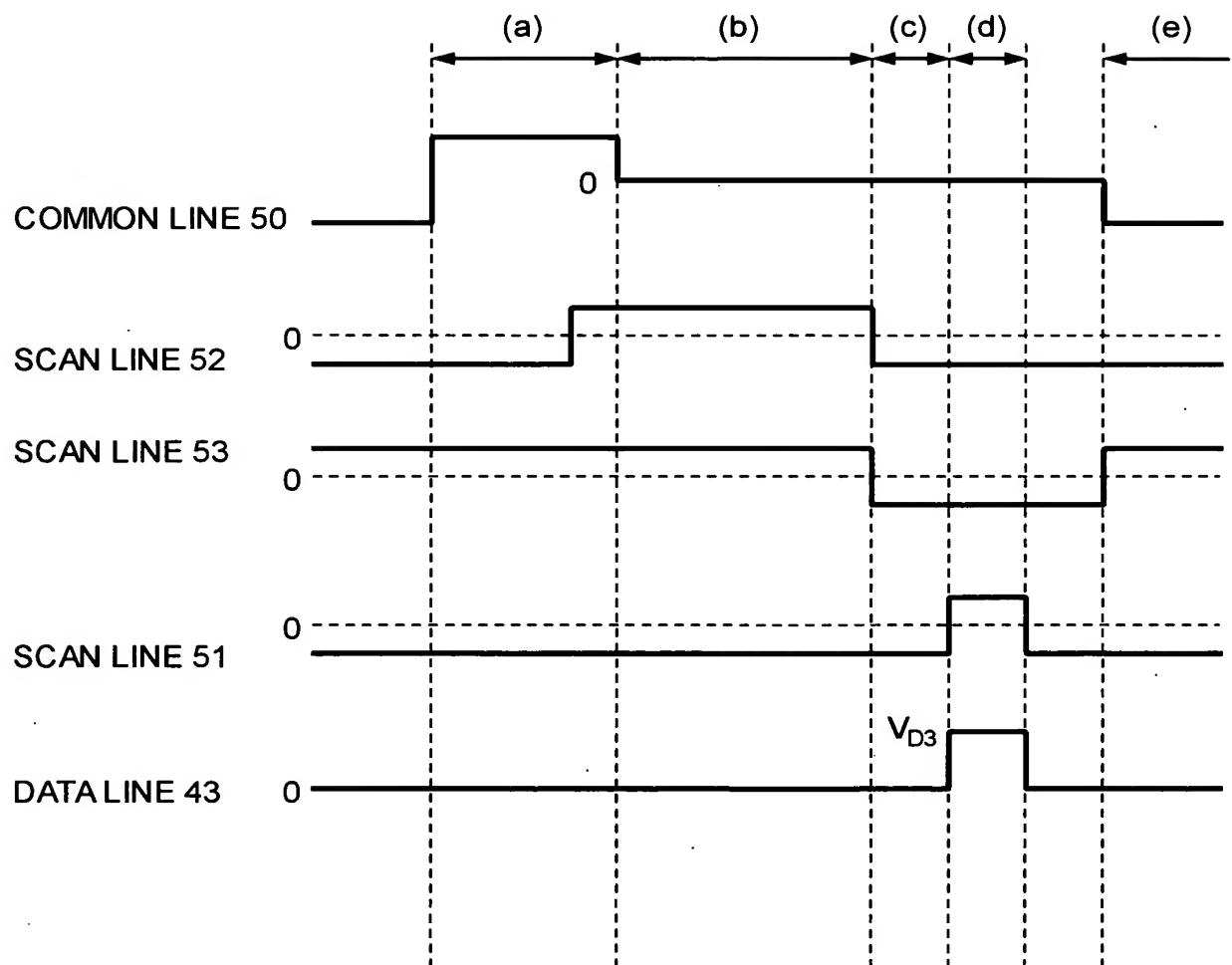


FIG. 19A

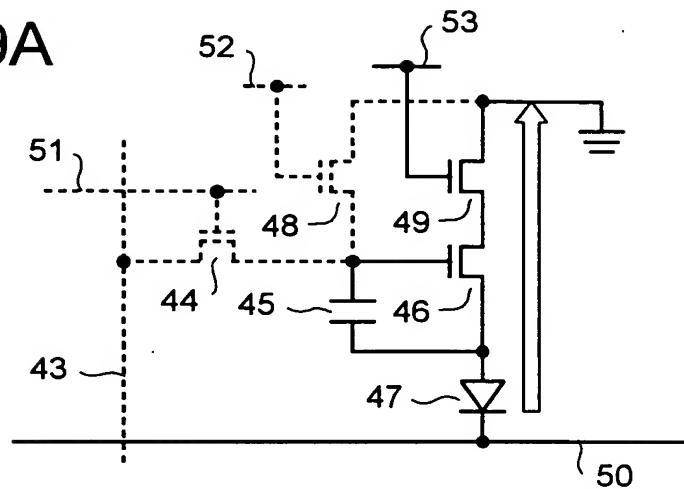
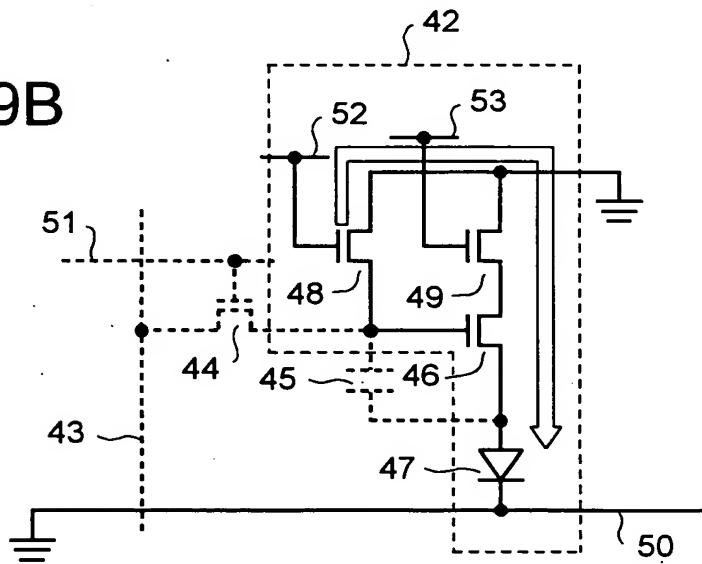


FIG. 19B



**FIG. 19C**

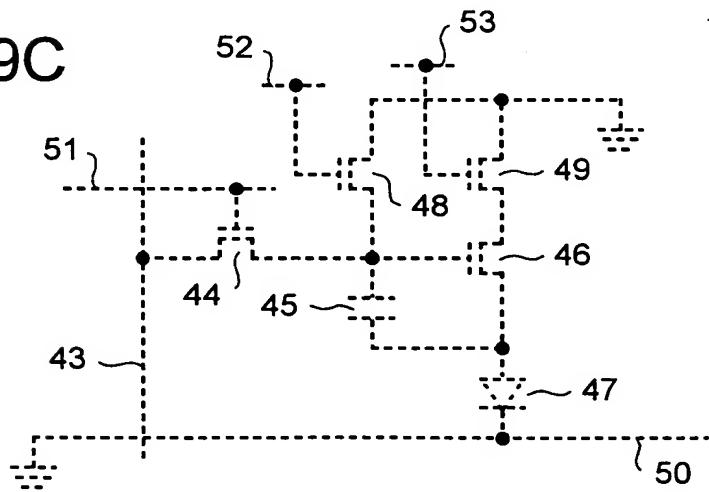


FIG.19D

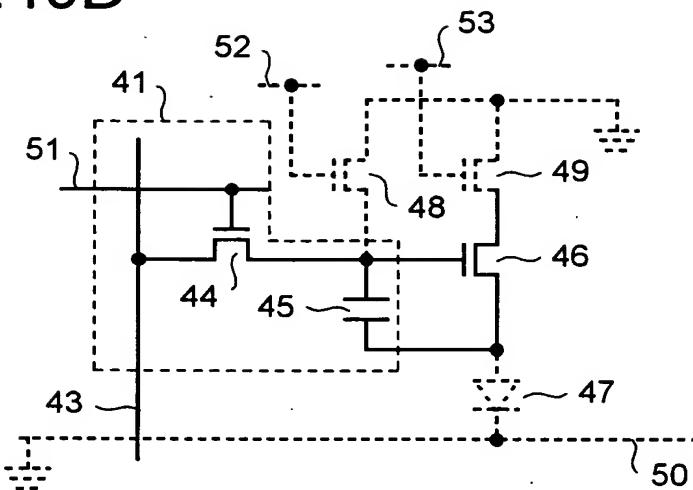


FIG.19E

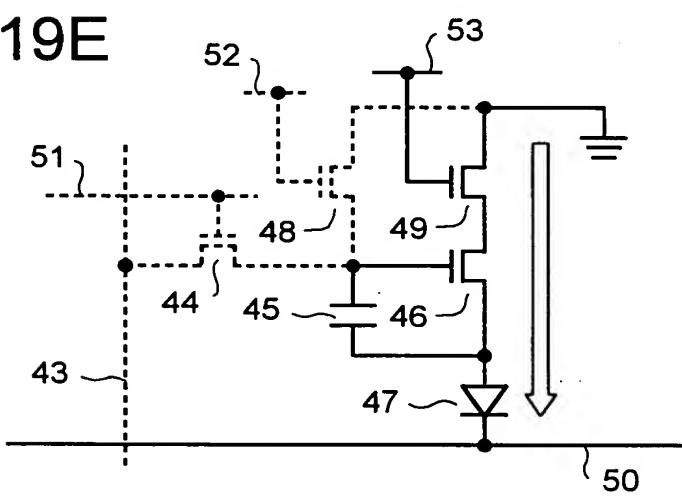


FIG.20

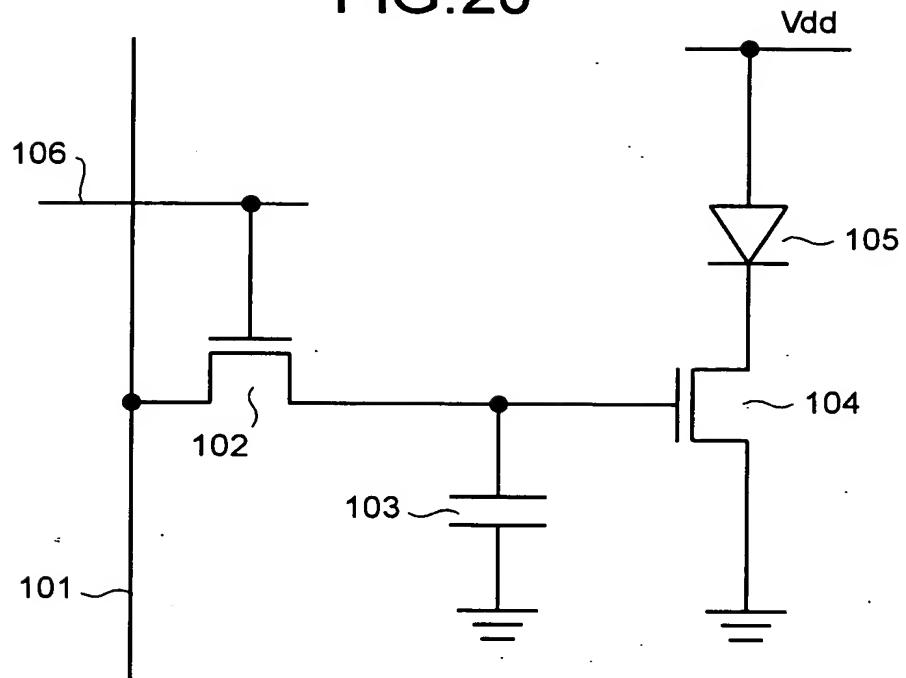


FIG.21

